

What is claimed is:

1. A circuit for detecting a shifted frequency,  
comprising:

5 a path selection unit for measuring a delay profile  
of a spread signal that has passed through a plurality of  
paths, and searching and selecting an optimum path from  
among said plurality of paths;

10 a plurality of finger processing units for reverse  
spreading the spread signal of each path, which is  
allocated by said path selection unit, by a spread code  
replica, obtaining a channel estimated value including at  
least phase variation component with respect to said path  
by using a given pilot symbol that is included in the  
signal after the reverse spread, and carrying out  
15 coherent detection by using said channel estimated value;

a phase difference measuring unit for measuring a  
phase difference from each phase variation component by  
each of said finger processing units;

20 a path timing difference measuring unit for  
measuring a periodical path timing difference depending  
on said delay profile;

a frequency error detecting unit for detecting a  
frequency error of said signal by using said path timing  
difference and said phase difference; and

25 a Doppler frequency detecting unit for detecting a  
Doppler frequency on the basis of said frequency error.

2. The circuit for detecting a shifted frequency  
according to claim 1, further comprising:

30 an average processing unit for averaging the  
frequency error from said frequency error detecting unit;

and

a calculating unit for obtaining a difference between the frequency error after said averaging and a current frequency error, wherein:

5       said Doppler frequency detecting unit generates information representing said Doppler frequency on the basis of the frequency error after said calculation by said calculating unit.

10      3. The circuit for detecting a shifted frequency according to claim 1, wherein:

      said phase difference measuring unit measures said phase difference by using only a phase variation component from said finger processing unit, to which a path having the maximum signal amplitude is allocated.

15      4. The circuit for detecting a shifted frequency according to claim 1, wherein:

      said phase difference measuring unit measures said phase difference by using a signal that is obtained by combining said each phase variation component in a maximum ratio corresponding to a signal amplitude of each path, which is allocated to each of said finger processing units.

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      5. The circuit for detecting a shifted frequency according to claim 1, wherein:

      each slot is provided with a plurality of said pilot symbols;

30      said phase difference measuring unit obtains a first phase difference that is measured from a phase variation

component between respective pilot symbols within one slot and a second phase difference that is measured from a phase variation component between respective pilot symbol groups within at least two slots; and

5       said frequency error detecting unit detects a large frequency error by using said first phase difference and detects a minute frequency error by using said second phase difference.

10 6. The circuit for detecting a shifted frequency according to claim 1, wherein:

      said path timing difference measuring unit measures said path timing difference by using only a delay profile corresponding to a path having a maximum signal amplitude, 15 which exceeds a given threshold.

7. The circuit for detecting a shifted frequency according to claim 1, wherein:

      said path timing difference measuring unit measures 20 a path timing difference with respect to all paths having a signal amplitude exceeding a given threshold and combining each path timing difference in a maximum ratio corresponding to the signal amplitude of each path.

25 8. The circuit for detecting a shifted frequency according to claim 1, wherein:

      said path selection unit averages said delay profile by a time period which is arbitrarily settable.

30 9. The circuit for detecting a shifted frequency according to claim 1, further comprising:

a combining unit for combining a signal after a coherent detection by each of finger processing units in a maximum ratio; and

5       a measuring unit for measuring a signal-to-interference ratio by using said signal combined in the maximum ratio, wherein:

10      said phase difference measuring unit generates reliability information of said phase difference by a measured value from said measuring unit to add the reliability information to said phase difference.

10. The circuit for detecting a shifted frequency according to claim 9, wherein:

15      said path timing difference measuring unit generates reliability information of said measured path timing difference and weights said measured path timing difference by said reliability information.

11. The circuit for detecting a shifted frequency according to claim 10, wherein:

20      said frequency error detecting unit compares the reliability information added to said phase difference with the reliability information added to said path timing difference and detects a frequency error by using either one of said phase difference and said path timing difference, which has a higher reliability.

12. The circuit for detecting a shifted frequency according to claim 10, wherein:

30      said frequency error detecting unit combines said phase difference and said path timing difference in the

maximum ratio by using the reliability information added to said phase difference and the reliability information added to said path timing difference as weight, respectively, and detects a frequency error from the information after said combining in the maximum ratio.

5        13. A method for detecting a shifted frequency, comprising the steps of:

10        measuring a delay profile of a spread signal that has passed through a plurality of paths, and searching and selecting an optimum path from among said plurality of paths;

15        reverse spreading the spread signal of each path, which is allocated by said path selection, using a spread code replica, and obtaining a channel estimated value including at least phase variation component with respect to said path by using a given pilot symbol that is included in the signal after the reverse spread;

20        carrying out finger processing to perform coherent detection by using said channel estimated value;

measuring a phase difference from each phase variation component based on each finger processing;

measuring a periodical path timing difference by said delay profile;

25        detecting a frequency error of said signal by using said path timing difference and said phase difference; and

detecting a Doppler frequency on the basis of said frequency error.

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14. The method for detecting a shifted frequency

according to claim 13, further comprising the steps of:  
averaging said frequency error; and  
obtaining a difference between the frequency error  
after said averaging and a current frequency error.

5 wherein:

upon detecting said Doppler frequency, information  
representing said Doppler frequency is generated on the  
basis of the frequency error after obtaining the  
difference.

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15. The method for detecting a shifted frequency  
according to claim 13, wherein:

upon measuring said phase difference, said phase  
difference is measured by using only a phase variation  
15 component by the finger processing, to which a path  
having a maximum signal amplitude is allocated.

16. The method for detecting a shifted frequency  
according to claim 13, wherein:

20 upon measuring said phase difference, said phase  
difference is measured by using a signal that is obtained  
by combining said each phase variation component in a  
maximum ratio corresponding to a signal amplitude of each  
path, which is allocated by each of said finger  
25 processing.

17. The method for detecting a shifted frequency  
according to claim 13, wherein:

each slot is provided with a plurality of said pilot  
30 symbols,  
upon measuring the phase difference, a first phase

difference that is measured from a phase variation component between respective pilot symbols within one slot and a second phase difference that is measured from a phase variation component between respective pilot symbol groups within at least two slots are obtained, and  
5 upon detecting said frequency error, a large frequency error is obtained by using said first phase difference, and a minute frequency error is obtained by using said second phase difference.

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18. The method for detecting a shifted frequency according to claim 13, wherein:

upon measuring said path timing difference, said path timing difference is measured by using only a delay profile corresponding to a path having a maximum signal amplitude, which exceeds a given threshold.  
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19. The method for detecting a shifted frequency according to claim 13, wherein:

20 upon measuring said path timing difference, a path timing difference with respect to all paths having a signal amplitude exceeding a given threshold is measured and each path timing difference is combined in a maximum ratio corresponding to the signal amplitude of each path.  
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20. The method for detecting a shifted frequency according to claim 13, wherein:

upon said path selection, said delay profile is averaged by a time period which is arbitrarily settable.

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21. The method for detecting a shifted frequency

according to claim 13, further comprising the steps of:  
combining a signal after a coherent detection by  
each of said finger processing in a maximum ratio; and  
measuring a signal-to-interference ratio by using  
5 said signal combined in the maximum ratio, wherein:  
upon measuring said phase difference, reliability  
information of said phase difference is generated from  
the measured value of the signal-to-interference ratio to  
add the reliability information to said phase difference.

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22. The method for detecting a shifted frequency  
according to claim 21, wherein:  
upon measuring said path timing difference,  
reliability information of said measured path timing  
15 difference is generated; and said measured path timing  
difference is weighted depending on said reliability  
information.

23. The method for detecting a shifted frequency  
20 according to claim 22, wherein:  
upon detecting said frequency error, the reliability  
information added to said phase difference is compared  
with the reliability information added to said path  
timing difference; and a frequency error is detected by  
25 using either one of said phase difference and said path  
timing difference, which has a higher reliability.

24. The method for detecting a shifted frequency  
according to claim 22, wherein:  
30 upon detecting said frequency error, said phase  
difference and said path timing difference are combined

in a maximum ratio by using the reliability information added to said phase difference and the reliability information added to said path timing difference as weight, respectively, and a frequency error is detected 5 from the value after being combined at the maximum ratio.

25. A portable communication apparatus having a circuit for detecting a shifted frequency, comprising:
- 10        a transmission and reception circuit for transmitting and receiving a signal that is spread by a spread code to and from a base station;
- 15        a path selection unit for measuring a delay profile of a received signal that has passed through a plurality of paths and has been received, and searching and selecting an optimum path from among said plurality of paths;
- 20        a plurality of finger processing units for reverse spreading a spread signal of each path, which is allocated by said path selection unit, using a spread code replica, obtaining a channel estimated value including at least phase variation component with respect to said path by using a given pilot symbol that is included in the signal after the reverse spread, and carrying out coherent detection by using said channel 25 estimated value;
- a phase difference measuring unit for measuring a phase difference from each phase variation component by each of said finger processing units;
- 30        a path timing difference measuring unit for measuring a periodical path timing difference depending on said delay profile;

a frequency error detecting unit for detecting a frequency error of said signal by using said path timing difference and said phase difference; and

5       a Doppler frequency detecting unit for detecting a Doppler frequency on the basis of said frequency error.

26. A portable communication apparatus, according to claim 25, wherein:

10      said path selection unit measures said delay profile by using a signal of a given common control channel as a phase reference for a downlink from the base station.

27. A portable communication apparatus, according to claim 25, wherein:

15      said path timing difference measuring unit measures said path timing difference by using a signal of a given common control channel as a phase reference for a downlink from the base station.

20    28. A portable communication apparatus, according to claim 25, wherein:

25      said phase difference measuring unit measures said phase difference from said phase variation component that is obtained from said pilot symbol that is included in a given individual channel of a downlink from the base station.

29. A portable communication apparatus, according to claim 25, further comprising:

30      a reference frequency signal generating unit for generating a reference frequency signal to be used upon

5        said transmission and reception; and  
          an average processing unit for averaging a frequency  
          error from said frequency error detecting unit; and  
          a frequency correction amount calculating unit for  
5        generating a correction amount to correct said reference  
          frequency signal from the frequency error after said  
          averaging.

10      30. A portable communication apparatus, according to  
          claim 29, wherein:  
          said average processing unit averages said frequency  
          error by a time period which is arbitrarily settable.

15      31. A portable communication apparatus, according to  
          claim 25, further comprising:  
          a control information generating unit for generating  
          control information to be used for at least control  
          channel of an uplink; and  
20      a control unit for controlling a transmission and  
          reception property of said transmission and reception  
          circuit, wherein:  
          said control information generating unit notifies  
          said base station side of information in accordance with  
          said detected Doppler frequency by inserting the  
25      information into a given individual control channel of an  
          uplink, and  
          said control unit optimum-controls a reception  
          property of said transmission and reception circuit in  
          response to a reply from the base station corresponding  
30      to said notification.

32. A portable communication apparatus, according to  
claim 25, further comprising:

a control information generating unit for generating  
control information to be used for at least an uplink  
5 control channel; and

a control unit for controlling a transmission and  
reception property of said transmission and reception  
circuit, wherein:

said control information generating unit judges  
10 whether or not a closed loop transmission diversity  
should be carried out in accordance with said detected  
Doppler frequency, notifies said base station side of  
information in accordance with its detection result by  
inserting the information into a given individual uplink  
15 control channel, and

said control unit optimally controls a reception  
property of said transmission and reception circuit in  
response to a reply from the base station corresponding  
to said notification.

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33. A portable communication apparatus, comprising:

a transmitting and receiving unit for transmitting  
and receiving a signal that is spread by a spread code to  
and from a base station;

25 a control unit for controlling a transmission and  
reception property of said transmitting and receiving  
unit;

a control information generating unit for generating  
control information to be used for at least an uplink  
30 control channel;

a Doppler frequency detecting unit for detecting a

Doppler frequency of a received signal; and  
a determination unit for determining whether or not  
a closed loop transmission diversity should be carried  
out in accordance with said detected Doppler frequency.

5 wherein:

said control information generating unit notifies  
said base station side of information corresponding to  
said determination result by inserting the information  
into the uplink control channel, and

10 said control unit optimum-controls a reception  
property of said transmitting and receiving unit in  
response to a reply from said base station corresponding  
to said notification.